Methods in quantum computing

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Class overview

- UTS undergraduates + SQA grads
- assumes familiarity with quantum computing and mathematical maturity (linear algebra, theorems and proofs)
- 12 weeks, 3 months, 3hour lecture + tutorial per week
- 3 types of assessments: problem sets, group video, final project
- 50% required to pass, attendance not required (but recommended)
- info on Canvas and

www.mariakieferova.com/methods-in-quantum-computing

• office hours immediately after class or by appointment

Topic overview

- 1. Quantum formalism, quantum mechanics and quantum information theory
- 2. Quantum stack physical implementation, architecture and quantum error correction
- 3. Quantum algorithm and complexity
- 4. Quantum communication and entanglement





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Introduce yourself to the class! Were are you from? What are your plans for this weekend?

- 1. Motivation behind quantum computing
- 2. Models of computation
- 3. Quantum circuits

Computational models



(a) Turing machine

(b) Conway's game of life



(c) Power Point

(d) Minecraft



Physics of Computation



Why are you interested in quantum computing?

What topics are you working on and why did you choose them?

Turing machine



- Γ a non-empty alphabet, i.e. a set of allowed symbols ۲
- $b \in \Gamma$ a blank symbol
- $\Sigma \subseteq \Gamma$ a set of symbols that initially appear on the tape
- Q a finite set of states of the machine
- $q_0 \in Q$ the initial state
- $F \subseteq Q$ set of accepting states. If the TM reaches one of these states, the computation finishes and the input is accepted ("yes").
- $\delta: Q \setminus F \times \Gamma \to Q \times \Gamma \times \{\text{left, right}\}\$ is the transition function. **xccin{0,1}**



Turing machines



not a theorem quantum computer can be simulated classically with exp overhead

A Turing machine can simulate any realistic model of computation.

computability

Extended Church–Turing thesis



Denote $\mathbb{B}^n := \mathbb{Z}_2^n$. Let $f : \mathbb{B}^n \to \mathbb{B}^m$ be a Boolean function that takes an *n*-bit string as input and outputs an *m*-bit string. Let \mathcal{G} be a collection of basic logic gates. A Boolean circuit for f is a sequence of gates $\{g_1, \cdots, g_L\} \in \mathcal{G}$ which converts an input $x \in \mathbb{B}^n$ to the output $y \in \mathbb{B}^m$ with a fixed size of K auxiliary bits. **JAND OR NOT**

Logical gates

Uniform circuit families



é

15 minutes



Show that the NAND and FANOUT (copy) are universal for

computation, i.e. they can be used to express all possible truth tables.

- (a) Show that the NOT gate can be simulated using a single NAND gate.
 (b) うのつ
- (b) Show that the AND gate can be simulated with a constant number of NAND gates.
- (c) Show that the OR gate can be simulated with a constant number of NAND gates. Hint: in the footnote ¹. How many NANDs is required for this construction?

You might use additional bits initialized to 0 or 1. 1 Use De Morgan's Law: A OR B = NOT (NOT A AND NOT B).

Logical circuits that can be inverted are known as reversible circuits.

$$a \wedge b = 1$$

$$a \wedge b = 0$$

$$a \wedge b = 0$$
input \rightarrow output
input

uv

What operations in Table 1 are reversible? What are the inverse operations to the reversible gates in Tables 1 and 2?

hot # inputs = # outputs

$$CNOT$$
 $O_L = \begin{pmatrix} 1 \\ 0 \end{pmatrix}$
 $O_L = \begin{pmatrix} 1 \\ 0$

Qubits

1.7 Ket Kel bra







Hello

Qubits



Given an initial state $|\psi_0\rangle$, we can apply a gate U to obtain a new state $|\psi_1\rangle$ $|\psi_1\rangle = U|\psi_0\rangle$. $|\psi_1\rangle = U|\psi_0\rangle$.

$$|\psi_{1}\rangle = U |\psi_{0}\rangle.$$

$$\begin{pmatrix} a \\ b \end{pmatrix} = \begin{pmatrix} c & d \\ e & f \end{pmatrix} \begin{pmatrix} g \\ h \end{pmatrix}$$

$$\begin{pmatrix} \end{pmatrix} = \begin{pmatrix} \end{pmatrix} \begin{pmatrix} d \\ h \end{pmatrix} \begin{pmatrix} d \\ h \end{pmatrix} \begin{pmatrix} d \\ h \end{pmatrix}$$

Show that quantum operations must be unitary in order to preserve the norm of quantum states. An operation U is unitary if and only it satisfies $U^{-1} = U^{\dagger}$.

- a Show that for a unitary matrix U, |det(U)| = 1. Hint: in a footnote.
- b A global phase of a quantum state is not detectable. In other words, states $|\psi\rangle$ and $e^{i\psi} |\psi\rangle$ represent the same physical state. What consequence will it have for single qubit gates?
- c Write the most general single qubit gate U using the convention det(U) = 1.

What is the state state prepared by this circuit? We can specify gates by defining how they act or basis state C H= Hadamardgate H107= 1/2 (0) +11) $H = \frac{1}{\sqrt{2}} \begin{pmatrix} 1 & 1 \\ 1 & -1 \end{pmatrix}$ $H(1) = \frac{1}{N_{21}}(107 - 117)$ $H_{1}^{1007} = \frac{1}{N_{22}} (107 + 117) 107 = \frac{1}{N_{22}} (1007 + 107) \\ CNOT (\frac{1}{N_{22}} (1007 + 107)) = \frac{1}{N_{22}} (1007 + 1117)$ Given any universal set of gates \mathcal{G} that is closed under inverse, any unitary operation $U \in SU(d)$ can be ε -approximated using $O(\log^c(\frac{1}{\varepsilon}))$ gates from \mathcal{G} for some constant c.



3. How would one construct CZ out of CNOT and single qubit gates?



$$Z = [0 \times 0] - [1 \times 1]$$

$$H = \frac{1}{12} (10 \times 0] + [1 \times 0] + [0 \times 1] - [1 \times 1])$$

$$CNOT = [0 \times 0] \otimes 1 + [1 \times 1] \otimes 1$$

$$H = [0 \times 0] + [1 \times 1]$$

$$C - Z = [0 \times 0] \otimes 1 + [1 \times 1] \otimes 2$$

$$= [0 \times 0] \otimes 10 \times 0] + [1 \times 1] \otimes 2$$

$$= [0 \times 0] \otimes 10 \times 0] + [1 \times 1] \otimes 1] + [1 \times 1] \otimes 1]$$